

Microwave Engineering
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Lecture 26
Design of single stage transistor amplifier

So, we have discussed the gain of general two-port circuits, and we have seen the different gain like power gain, available gain, transducer gain. And, we have discussed the conditions related to the stability of the two-port. We have also discussed the conditions related to the stability of the two-port device. Let us now move on to discussion of the issues related to the design of a single-stage transistor amplifier.

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$$\Gamma_{in} = \Gamma_S^*$$

$$\Gamma_{out} = \Gamma_L^*$$

$$G_{T_{max}} = \frac{1}{1 - |\Gamma_S|^2} |S_{21}|^2 \frac{1 - |\Gamma_L|^2}{|1 - S_{22}\Gamma_L|^2}$$

Single Stage Transistor Amplifier Design

Maximum power transfer from the input matching network to the transistor and the maximum power transfer from the transistor to the output matching network will occur when:

$$\Gamma_{in} = \Gamma_S^*$$

$$\Gamma_{out} = \Gamma_L^*$$

Then, assuming lossless matching sections, these conditions will maximize the overall transducer gain:

$$G_{T_{max}} = \frac{1}{1 - |\Gamma_S|^2} |S_{21}|^2 \frac{1 - |\Gamma_L|^2}{|1 - S_{22}\Gamma_L|^2}$$

So, the maximum power transfer from the input matching network to the transistor and maximum power transfer from the transistor to the output matching network will occur when both input and output, they are conjugately matched. That is, gamma is equal to gamma s conjugate, and gamma out is equal to gamma l conjugate. We assumed that the matching network that we are designing, these are lossless matching network and, with the assumptions of lossless matching sections.

These conditions will maximize the overall transducer gain, and we can write Γ_S max, the maximum of the transducer gain as $1 - |S_{22}|^2 - |S_{21}|^2 |\Gamma_L|^2$ divided by $1 - |S_{11}|^2 - |S_{12}|^2 |\Gamma_S|^2$.

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$$\Gamma_S^* = S_{11} + \frac{S_{12} S_{21} \Gamma_L}{1 - S_{22} \Gamma_L}$$

$$\Gamma_L^* = S_{22} + \frac{S_{12} S_{21} \Gamma_S}{1 - S_{11} \Gamma_S}$$

Single Stage Transistor Amplifier Design

In the general case with a bilateral transistor, Γ_{in} is affected by Γ_{out} and vice versa, so that the input and output sections must be matched simultaneously.

$$\Gamma_S^* = S_{11} + \frac{S_{12} S_{21} \Gamma_L}{1 - S_{22} \Gamma_L}$$

$$\Gamma_L^* = S_{22} + \frac{S_{12} S_{21} \Gamma_S}{1 - S_{11} \Gamma_S}$$

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So, in the general case with a bilateral transistor, Γ_S is affected by Γ_L and vice versa. So, the input and output sections are required to be matched simultaneously. That means Γ_S will be affected by Γ_L ; Γ_L will be affected by any changes that we make in Γ_S . So, we can write from our earlier discussion that Γ_S is equal to Γ_S^* conjugate.

Therefore, Γ_S^* can be written as $S_{11} + S_{12} S_{21} \Gamma_L / (1 - S_{22} \Gamma_L)$. This is essentially the expression of Γ_S . And, similarly, we can write Γ_L^* conjugate is equal to $S_{22} + S_{12} S_{21} \Gamma_S / (1 - S_{11} \Gamma_S)$. So, this right-hand side is essentially the expression for Γ_L .

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$$\Gamma_s = \frac{B_1 \pm \sqrt{B_1^2 - 4|C_1|^2}}{2C_1}$$

$$\Gamma_L = \frac{B_2 \pm \sqrt{B_2^2 - 4|C_2|^2}}{2C_2}$$

$$B_1 = 1 + |S_{11}|^2 - |S_{22}|^2 - |\Delta|^2$$

$$B_2 = 1 + |S_{22}|^2 - |S_{11}|^2 - |\Delta|^2$$

$$C_1 = S_{11} - \Delta S_{22}^*$$

$$C_2 = S_{22} - \Delta S_{11}^*$$

Single Stage Transistor Amplifier Design

The solution is:

$$\Gamma_s = \frac{B_1 \pm \sqrt{B_1^2 - 4|C_1|^2}}{2C_1}$$

$$\Gamma_L = \frac{B_2 \pm \sqrt{B_2^2 - 4|C_2|^2}}{2C_2}$$

$$B_1 = 1 + |S_{11}|^2 - |S_{22}|^2 - |\Delta|^2$$

$$B_2 = 1 + |S_{22}|^2 - |S_{11}|^2 - |\Delta|^2$$

$$C_1 = S_{11} - \Delta S_{22}^*$$

$$C_2 = S_{22} - \Delta S_{11}^*$$

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From these conditions we can solve for gamma s and gamma l. The solution for gamma s is B₁ plus-minus root B₁ square minus 4 mod C₁ square divided by 2 C₁. And, gamma l is equal to B₂ plus-minus root B₂ square minus 4 mod C₂ square divided by 2 C₂. Here this B₁ B₂, C₁, and C₂ these are given by B₁ is equal to 1 plus mod S₁₁ square minus mod S₂₂ square minus mod of delta square. B₂ is 1 plus mod S₂₂ square minus mod S₁₁ square minus mod of delta square. And, C₁ is equal to S₁₁ minus delta S₂₂ conjugate C₂ is S₂₂ minus delta S₁₁ conjugate.

So, given the s parameters of the transistor, we can see that we can get the values of gamma s and gamma l. Things become very much simplified if we have a unilateral transistor that means. S₁₂ is equal to 0. In that case we get gamma S equal to S₁₁ conjugate and gamma l is equal to S₂₂ conjugate. Because gamma in becomes is equal to S₁₁ in case of a unilateral device. S₁₂ is equal to 0 and gamma out becomes S₂₂. Now, once we have this gamma s and gamma l computed.

Now, this at the reflection coefficients which had to be seen looking into the matching networks at both ends. And, therefore, we can start with Z naught and design a matching network to transform it into an impedance which will give us the required gamma s and gamma l.

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$$G_{TU_{\max}} = \frac{1}{1 - |S_{11}|^2} |S_{21}|^2 \frac{1}{1 - |S_{22}|^2}$$

$$G_{T_{\max}} = \frac{|S_{21}|}{|S_{12}|} \left(K - \sqrt{K^2 - 1} \right)$$

$$G_{msg} = \frac{|S_{21}|}{|S_{12}|}$$

Single Stage Transistor Amplifier Design

When $S_{12} = 0$, it shows that $\Gamma_S = S_{11}^*$ and $\Gamma_L = S_{22}^*$, and the maximum transducer gain for unilateral case:

$$G_{TU_{\max}} = \frac{1}{1 - |S_{11}|^2} |S_{21}|^2 \frac{1}{1 - |S_{22}|^2}$$

When the transistor is unconditionally stable, $K > 1$, and the max transducer power gain can be simply re-written as:

$$G_{T_{\max}} = \frac{|S_{21}|}{|S_{12}|} \left(K - \sqrt{K^2 - 1} \right)$$

The maximum stable gain with $K = 1$:

$$G_{msg} = \frac{|S_{21}|}{|S_{12}|}$$

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For a unilateral transistor, $G_{TU_{\max}}$ is $\frac{1}{1 - |S_{11}|^2} |S_{21}|^2 \frac{1}{1 - |S_{22}|^2}$. So, when the transistor is unconditionally stable K is greater than 1. And, the maximum transducer power gain can be simply rewritten as $G_{T_{\max}}$ is $\frac{|S_{21}|}{|S_{12}|} \left(K - \sqrt{K^2 - 1} \right)$. And, we define maximum stable gain that we can get is for K is equal to 1 and this is G_{msg} is $\frac{|S_{21}|}{|S_{12}|}$. Please note that when S_{12} is equal to 0 we have a unilateral case, and that is written separately here.

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Steps for Amplifier Design for Maximum Gain

To design an amplifier for maximum gain at a given frequency, we need to perform the following steps:

- ❑ Check for the stability of the transistor
- ❑ Unconditionally stable devices can always be conjugately matched for maximum gain
- ❑ Find Γ_S and Γ_L
- ❑ Calculate G_S , G_0 and G_L
- ❑ Matching network can be found out using Smith Chart

So, we can follow the steps for an amplifier design for maximum gain. We check for the stability of the transistor. We have seen that unconditionally stable devices can always be conjugately matched for maximum gain. So, we find out from the given s-parameters of the transistors at the specified frequency. We find out Γ_S and Γ_L . And, then we can calculate G_S , G_0 , and G_L , and product of these three will give us the overall gain.

And, since we know Γ_S and Γ_L we can design the matching network using Smith chart by transforming Z_{in} by the appropriate matching network. Now, matching networks can be designed in different ways; we can use section of transmission lines and stubs, we can use L section design that we have discussed. So, design of the matching network is left to the choice of the designer and also the form in which the fabrication will be simpler. Now, we have discussed how we can design an amplifier for maximum gain.

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$$G_S = \frac{1 - |\Gamma_S|^2}{|1 - S_{11}\Gamma_S|^2}$$

$$G_L = \frac{1 - |\Gamma_L|^2}{|1 - S_{22}\Gamma_L|^2}$$

$$G_{S_{max}} = \frac{1}{1 - |S_{11}|^2}$$

$$G_{L_{max}} = \frac{1}{1 - |S_{22}|^2}$$

$$g_S = \frac{G_S}{G_{S_{max}}} = \frac{1 - |\Gamma_S|^2}{|1 - S_{11}\Gamma_S|^2} (1 - |S_{11}|^2)$$

$$g_L = \frac{G_L}{G_{L_{max}}} = \frac{1 - |\Gamma_L|^2}{|1 - S_{22}\Gamma_L|^2} (1 - |S_{22}|^2)$$

Constant-Gain Circles and Design for Specific Gain

In some cases it is preferable to design for less than the maximum obtainable gain, in order to improve bandwidth or to obtain a specific value of amplifier gain.
This can be done by designing the input and output matching sections to have less than maximum gains.

The design procedure is facilitated by plotting *constant-gain circles* on the Smith chart to represent loci of Γ_S and Γ_L that give fixed values of gain G_S and G_L . The procedure is illustrated with unilateral case.

$$G_S = \frac{1 - |\Gamma_S|^2}{|1 - S_{11}\Gamma_S|^2} \quad G_{S_{max}} = \frac{1}{1 - |S_{11}|^2}$$

$$G_L = \frac{1 - |\Gamma_L|^2}{|1 - S_{22}\Gamma_L|^2} \quad G_{L_{max}} = \frac{1}{1 - |S_{22}|^2}$$

These gains are maximized when $\Gamma_S = S_{11}^*$ and $\Gamma_L = S_{22}^*$

$$g_S = \frac{G_S}{G_{S_{max}}} = \frac{1 - |\Gamma_S|^2}{|1 - S_{11}\Gamma_S|^2} (1 - |S_{11}|^2)$$

$$g_L = \frac{G_L}{G_{L_{max}}} = \frac{1 - |\Gamma_L|^2}{|1 - S_{22}\Gamma_L|^2} (1 - |S_{22}|^2)$$

But, in certain applications, it may be preferable to design less than the maximum obtainable gain. For example, if we know that the higher the gain, the bandwidth will lower. So, in order to improve the bandwidth, we may sacrifice for gain. Or this amplifier may be working as a part of system where we require the amplifier to provide some specific values of gain. So, we have seen that G_0 is essentially mod of S_{21} square. However we can achieve gains lower than the maximum achievable gain.

By designing and input and output matching sections to have gained less than there maximum values. That means instead of designing G_s and G_l for the maximum values we can design them for lower values and achieve the desired gain. The design procedure is facilitated by plotting

constant gain circles on the Smith chart to represent loci of Γ_S and Γ_L . That gives fixed values of gains G_S and G_L . So, we illustrate this procedure with a unilateral case.

So, we have G_S given by this expression, G_L given by this; G_S is equal to $1 - \text{mod of } \Gamma_S$ square divided by $\text{mod of } 1 - S_{11} \Gamma_S$ square. Similarly, G_L is $1 - \text{mod of } \Gamma_L$ square divided by $\text{mod of } 1 - S_{22} \Gamma_L$ square. And, therefore, we have G_S max to be equal to $1 - \text{mod } S_{11}$ square. That means when we have since we have unilateral transistor, Γ_S is now equal to S_{11} , and Γ_S conjugate becomes equal to Γ_S , and therefore, this Γ_S can be replaced by S_{11} conjugate.

In the same manner, Γ_L becomes S_{22} when S_1 to S_0 . Therefore, G_L max becomes $1 - \text{mod of } S_{22}$ square. Now, we define small G_S is equal to G_S by G_S max, and this can be written as $1 - \text{mod } \Gamma_S$ square divided and by $1 - \text{mod } S_{11} \Gamma_S$ square into $1 - \text{mod } S_{11}$ square. And, similarly, we define the normalized G_L , capital G_L divided G_L max is equal to small G_L and its corresponding expression.

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$$C_S = \frac{g_S S_{11}^*}{1 - (1 - g_S) |S_{11}|^2}$$

$$R_S = \frac{\sqrt{1 - g_S} (1 - |S_{11}|^2)}{1 - (1 - g_S) |S_{11}|^2}$$

$$C_L = \frac{g_L S_{22}^*}{1 - (1 - g_L) |S_{22}|^2}$$

$$R_L = \frac{\sqrt{1 - g_L} (1 - |S_{22}|^2)}{1 - (1 - g_L) |S_{22}|^2}$$

Constant-Gain Circles and Design for Specific Gain

For fixed values of g_S and g_L , represents circles in the Γ_S or Γ_L plane.

$$C_S = \frac{g_S S_{11}^*}{1 - (1 - g_S) |S_{11}|^2} \quad C_L = \frac{g_L S_{22}^*}{1 - (1 - g_L) |S_{22}|^2}$$

$$R_S = \frac{\sqrt{1 - g_S} (1 - |S_{11}|^2)}{1 - (1 - g_S) |S_{11}|^2} \quad R_L = \frac{\sqrt{1 - g_L} (1 - |S_{22}|^2)}{1 - (1 - g_L) |S_{22}|^2}$$

- The centres of each family of circles lie along straight lines given by the angle of S_{11}^* or S_{22}^*
- Γ_S and Γ_L can be chosen along these circles to provide the desired gains. The choices for Γ_S and Γ_L are not unique, but it makes sense to choose points close to the centre of the Smith chart to minimize mismatch

Now, if we fix values of G_s and G_l , they represent circles in Γ_s or Γ_l plane. And their centers and radius are given by $C_s R_s$ for the G_s constant G_s and $C_l R_l$ for constant G_l . Now, we can see that the center of each family of circles, so for different values of G_s and G_l we can have a family of constant gain circles. And, the centers of each family of circles lie along a straight line given by angles of S_{11} conjugate or S_{22} conjugate. Γ_s and Γ_l can be chosen along these circles to provide the desired gains.

So, we can plot this circle's family of circles in the Smith chart. And, for a given G_s or G_l we can find out the corresponding Γ_s , directly from the Γ_l or directly from the Smith chart. The choices for Γ_s and Γ_l are not unique because we are representing constant gain circles. So, any point on that circle is a candidate but, it makes sense to choose funds closer to the center of the Smith chart to minimize mismatch.

So, these Γ_s and Γ_l are chosen in these circles. So, that it is closest to the center of the smith chart, and therefore, the mismatch is reduced. We explain here with some numerical values.

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$$G_{S_{max}} = \frac{1}{1 - |S_{11}|^2} = 2.29 = 3.6 \text{ dB}$$

$$G_{L_{max}} = \frac{1}{1 - |S_{22}|^2} = 1.56 = 1.9 \text{ dB}$$

Constant-Gain Circles and Design for Specific Gain

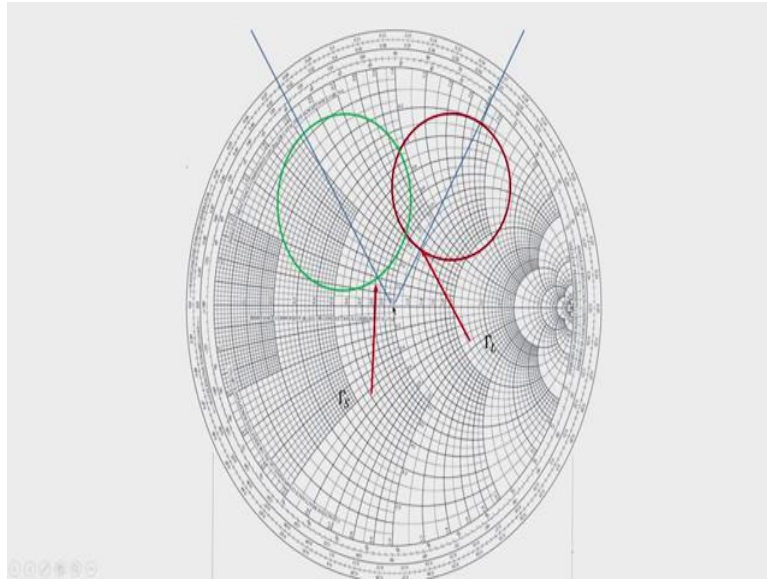
For example, if an amplifier is to be designed to provide a gain of 12 dB, and at the design frequency $S_{11} = 0.75\angle -120^\circ$, $S_{21} = 2.83\angle 80^\circ$ and $S_{22} = 0.6\angle -70^\circ$

We find that $G_0 = |S_{21}|^2 = 8.01 = 9 \text{ dB}$

$$G_{S_{max}} = \frac{1}{1 - |S_{11}|^2} = 2.29 = 3.6 \text{ dB} \qquad G_{L_{max}} = \frac{1}{1 - |S_{22}|^2} = 1.56 = 1.9 \text{ dB}$$
$$G_S = 2 \text{ dB} \qquad G_L = 1 \text{ dB}$$

For example, if an amplifier is designed to provide a gain of 12 dB and at the design frequency. Suppose we have S_{11} is equal to 0.75, angle is minus 120 degrees S_{21} is 2.83 with angle 80 degrees, and S_{22} is 0.6 with angle 70 degrees. And, we have already mentioned that S_{12} is 0. In that case we find that G_0 is roughly 9 dB, and G_S max is 3.6 dB, and G_L max is 1.9 dB. So, what we can do in order to realize this 12 dB gain, we can choose G_S to be 2 dB and G_L to be 1 dB. And, also we know that this constant G_L circle their center will now lie at 120 degrees. Because it lies along the line defining the angle of S_{11} conjugate. Similarly, the other circle will lie at 70 degrees.

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So, these are not exact values just to show the representative values of how we can use this information. For, example this will be G_s circle this maybe G_l circle. And, the points that are closest to the center of the Smith chart and intersecting with the circle is this. Similarly, this is the closest point intersecting with constant G_l circle. And nearest to the Smith chart, so we will choose this value of γ_l and this value as γ_s . Once, again it should be noted that this circle has not been drawn to the scale this is just for representation purposes only.

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$$F = \frac{S_i/N_i}{S_o/N_o} \geq 1$$

$$F = F_{min} + \frac{R_N}{G_S} |Y_S - Y_{opt}|^2$$

Low Noise Design

Besides stability and gain, another important design consideration for a microwave amplifier is its noise figure. In receiver applications especially it is often required to have a preamplifier with as low a noise figure as possible.

$$F = \frac{S_i/N_i}{S_o/N_o} \geq 1$$

Generally it is not possible to obtain both minimum noise figure and maximum gain for an amplifier, so some compromise is made. This can be done by using constant-gain circles and *circles of constant noise figure* to select a usable trade-off between noise figure and gain.

Next, we move on to low noise design. So, we have seen the two cases of single-stage transistor amplifier design. Designing for maximum gain and designing for specified gain. Let us now consider another issue the low noise design. So, besides stability and gain another important design consideration for a microwave amplifier is its noise figure. Particularly in receiver applications it is often required to have a preamplifier with a noise figure as low as possible.

Now, we know that the amplifiers add noise, and the noise figure is defined as the signal to noise ratio at the input divided by signal to noise ratio at the output of the system. And, usually it is greater than 1 because of S_{naught} by N_{naught} . At the output of the amplifier becomes lower than S_i by N_i at the input of the amplifier.

So, while designing microwave amplifiers, it is not possible to obtain both minimum noise figure and maximum gain. Some compromise is made where we operate the amplifier at slightly reduces gain to achieve a specified noise figure. And, this can be done by using constant gain circles and circles of constant noise figure to select a suitable rate of between noise figure and gain.

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$$F = F_{min} + \frac{R_N}{G_S} |Y_S - Y_{opt}|^2$$

Low Noise Design

Noise figure of a two-port amplifier can be expressed as

$$F = F_{min} + \frac{R_N}{G_S} |Y_S - Y_{opt}|^2$$

where,

- $Y_S = G_S + jB_S$ = source admittance presented to transistor
- Y_{opt} = optimum source admittance that results in minimum noise figure
- F_{min} = minimum noise figure of transistor, attained when $Y_S = Y_{opt}$
- R_N = equivalent noise resistance of transistor
- G_S = real part of source admittance

Ref. D. M. Pozar, "Microwave Engineering", 4th ed.

A noise figure of two-port amplifier can be expressed as F equal to F_{min} , minimum noise figure plus R_N by G_S into mod of Y_S minus Y_{opt} square. Now, this $Y_S = G_S + jB_S$ is a source admittance. Y_{opt} the optimum source admittance, which results in a minimum noise figure. So, we can see that when Y_S is equal to Y_{opt} F becomes equal to F_{min} . And, F_{min} minimum

noise of the transistor, R_n is equivalent noise resistance of the transistor. And, G_s is the real part of the source admittance.

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$$Y_S = \frac{1}{Z_0} \frac{1 - \Gamma_S}{1 + \Gamma_S}$$

$$Y_{opt} = \frac{1}{Z_0} \frac{1 - \Gamma_{opt}}{1 + \Gamma_{opt}}$$

$$|Y_S - Y_{opt}|^2 = \frac{4}{Z_0^2} \frac{|\Gamma_S - \Gamma_{opt}|^2}{|1 + \Gamma_S|^2 |1 + \Gamma_{opt}|^2}$$

$$G_S = \text{Re}(Y_S) = \frac{1}{Z_0} \frac{1 - |\Gamma_S|^2}{|1 + \Gamma_S|^2}$$

$$F = F_{min} + \frac{R_N}{G_S} |Y_S - Y_{opt}|^2 \text{ can be written as } F = F_{min} + \frac{4R_N}{Z_0} \frac{|\Gamma_S - \Gamma_{opt}|^2}{(1 - |\Gamma_S|^2) |1 + \Gamma_{opt}|^2}$$

Low Noise Design

The quantities F_{min} , Γ_{opt} and R_N are the characteristics of the particular transistor being used, and these are called the *noise parameters* of the device. These are either provided by the manufacturer or measured.

$$Y_S = \frac{1}{Z_0} \frac{1 - \Gamma_S}{1 + \Gamma_S}$$

$$Y_{opt} = \frac{1}{Z_0} \frac{1 - \Gamma_{opt}}{1 + \Gamma_{opt}}$$

$$|Y_S - Y_{opt}|^2 = \frac{4}{Z_0^2} \frac{|\Gamma_S - \Gamma_{opt}|^2}{|1 + \Gamma_S|^2 |1 + \Gamma_{opt}|^2}$$

$$G_S = \text{Re}(Y_S) = \frac{1}{Z_0} \frac{1 - |\Gamma_S|^2}{|1 + \Gamma_S|^2}$$

$$F = F_{min} + \frac{R_N}{G_S} |Y_S - Y_{opt}|^2 \text{ can be written as } F = F_{min} + \frac{4R_N}{Z_0} \frac{|\Gamma_S - \Gamma_{opt}|^2}{(1 - |\Gamma_S|^2) |1 + \Gamma_{opt}|^2}$$

The quantities F_{min} , Γ_{opt} as you will see it is related to Y_{opt} , and R_n are the characteristics of the particular transistor been used. And, these are called the noise parameters of the device. Now, these parameters can be either provided by the manufacturer in the data sheet or this can be measured. So, we have Y_S related to Γ_S as $\frac{1}{Z_0} \frac{1 - \Gamma_S}{1 + \Gamma_S}$. In the same manner, Y_{opt} related to Γ_{opt} as $\frac{1}{Z_0} \frac{1 - \Gamma_{opt}}{1 + \Gamma_{opt}}$.

Now, in the expression, for we have mod of $Y_S - Y_{opt}$ square, so this term can be calculated from Y_S and Y_{opt} . And, also G_S which is the real part of Y_S , can be calculated. And,

then in the expression for F we substitute Γ_s and mod of Γ_s minus Γ_{opt} square from here. And, then we get the expression for F as F_{min} plus $4 R_n$ divided by Z_0 into Γ_s minus Γ_{opt} magnitude square divided by $1 - |\Gamma_s|^2$ into $1 + |\Gamma_{opt}|^2$.

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$$F = F_{min} + \frac{4R_N}{Z_0} \frac{|\Gamma_s - \Gamma_{opt}|^2}{(1 - |\Gamma_s|^2)|1 + \Gamma_{opt}|^2}$$

$$N = \frac{|\Gamma_s - \Gamma_{opt}|^2}{1 - |\Gamma_s|^2} = \frac{F - F_{min}}{4R_N/Z_0} |1 + \Gamma_{opt}|^2$$

$N = \frac{|\Gamma_s - \Gamma_{opt}|^2}{1 - |\Gamma_s|^2}$ can be written as $(\Gamma_s - \Gamma_{opt})(\Gamma_s^* - \Gamma_{opt}^*) = N(1 - |\Gamma_s|^2)$ from which we get

$$\left| \Gamma_s - \frac{\Gamma_{opt}}{N+1} \right| = \frac{\sqrt{N(N+1 - |\Gamma_{opt}|^2)}}{(N+1)}$$
 is the equation of a circle

$$C_F = \frac{\Gamma_{opt}}{N+1}$$
 is the center and $R_F = \frac{\sqrt{N(N+1 - |\Gamma_{opt}|^2)}}{(N+1)}$ is the radius

Low Noise Design

$$F = F_{min} + \frac{4R_N}{Z_0} \frac{|\Gamma_s - \Gamma_{opt}|^2}{(1 - |\Gamma_s|^2)|1 + \Gamma_{opt}|^2}$$

For a fixed noise figure F a circle in the Γ_s plane can be obtained. The *noise figure parameter*, N , as

$$N = \frac{|\Gamma_s - \Gamma_{opt}|^2}{1 - |\Gamma_s|^2} = \frac{F - F_{min}}{4R_N/Z_0} |1 + \Gamma_{opt}|^2$$

$N = \frac{|\Gamma_s - \Gamma_{opt}|^2}{1 - |\Gamma_s|^2}$ can be written as $(\Gamma_s - \Gamma_{opt})(\Gamma_s^* - \Gamma_{opt}^*) = N(1 - |\Gamma_s|^2)$ from which we get

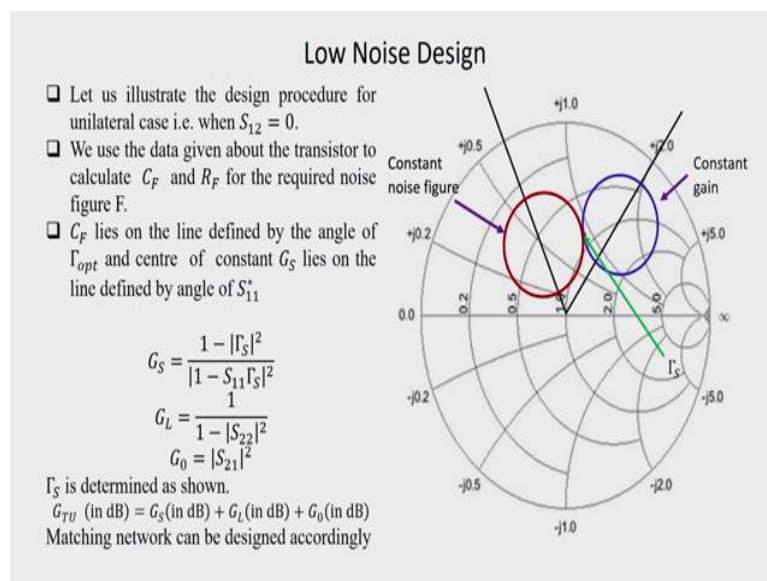
$$\left| \Gamma_s - \frac{\Gamma_{opt}}{N+1} \right| = \frac{\sqrt{N(N+1 - |\Gamma_{opt}|^2)}}{(N+1)}$$
 is the equation of a circle

$C_F = \frac{\Gamma_{opt}}{N+1}$ is the center and $R_F = \frac{\sqrt{N(N+1 - |\Gamma_{opt}|^2)}}{(N+1)}$ is the radius

Now, for a fixed noise figure F , a circle in the Γ_s plane can be obtained from this expression. So, a parameter n , which is the noise figure parameter, is introduced. And, it is defined as mod Γ_s minus Γ_{opt} square divided by $1 - \text{mod of } \Gamma_s \text{ square}$. So, essentially the ratio of these two terms and then we get this equal to $F - F_{min}$ divided by $4 R_n$ divided by Z_0 . Mod of $1 + \Gamma_{opt}$ square. Now, this expression N is equal to Γ_s minus Γ_{opt} magnitude square divided by $1 - \text{mod } \Gamma_s \text{ square}$.

It can be written in this form $\Gamma_S - \Gamma_{opt}$ into Γ_S conjugate minus Γ_{opt} conjugate is equal to N into $1 - \text{magnitude of } \Gamma_S \text{ square}$. And, from this expression, we can actually get the equation of the circle. And, the center of this circle C_f is Γ_{opt} divided by $N + 1$, and radius of the circle R_f is given by square root of N into $N + 1$ minus Γ_{opt} square divided by $N + 1$. Now, we see that Γ_{opt} a complex quantity, and this center C_f will lie along a line defined by the angle of Γ_{opt} in the Smith chart.

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So, let us illustrate the design procedure for the unilateral case that is when S_{12} to equal to 0. We use the data given about the transistor to calculate C_f and R_f for the required noise figure F . C_f lies on the line defined by the angle of Γ_{opt} and center of constant G_S circle lies on the line defined by the angle of S_{11} conjugate. So, these are the two circles. This is the G_S circle just touching the constant the circle corresponding to the required noise figure F . And, this point of intersection is the Γ_S , which actually gives the value of G_S for the required F .

And, once we have this Γ_S we can find out G_S . G_L we can find out to be $1 / (1 - \text{mod of } S_{22} \text{ square})$. Because S_{12} is 0 and G_{naught} is given by $\text{mod of } S_{21} \text{ square}$, and we can calculate G_{TU} in dB as sum of G_S , G_L and G_{naught} in dB. So, now we have the gain of G_S but designed in such a way it satisfies the requirement of the noise figure F . Once, we have Γ_S and Γ_L , we can see that Γ_S value we have already calculated. And, once we have this Γ_S and Γ_L , we can design a matching network following the procedure.

That is used to transform Z_{naught} to either Γ_S or Γ_L using stub section of transmission lines or plumbed elements. So, this is in brief how we can design an amplifier

with a specified noise figure. And, that design is achieved by adjusting the gain G_s , and we cannot have maximum gain from such design. So, we have discussed different issues related to the design of amplifiers. So, in the next lecture we will discuss R_f oscillators and will see some of the steps involved in design of such oscillators.