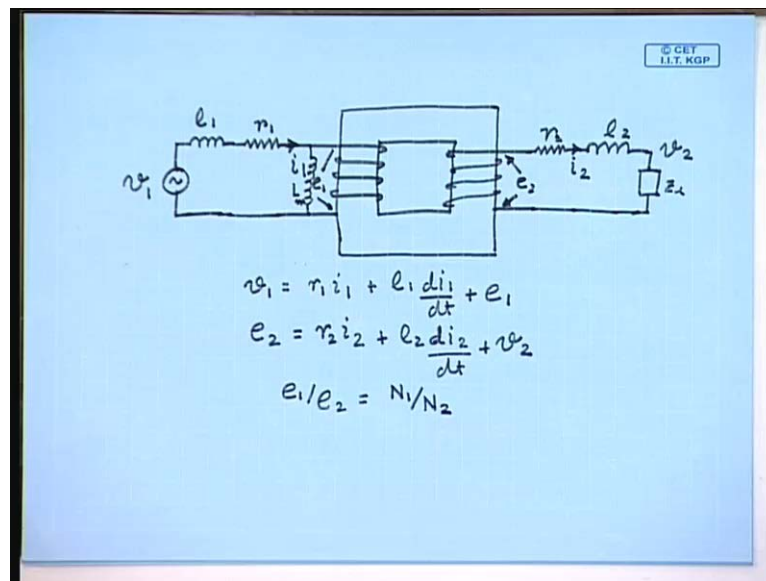


Electrical Machines - I
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Lecture - 3
Modeling of Single Phase Transformers

In the last lecture, we have shown how to relax two of the assumptions that we made regarding an ideal transformer that is of lossless winding, and no leakage.

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And we found that a practical transformer more rule can be derived from an ideal transformer which is represented by a core and two windings incorporating the resistance of the windings, and the effect of leakage flux can be incorporated by assuming in leakage inductance in series with both the windings r_1, r_2, l_1, l_2, v_1 ; the load you usually connect be connected here equal to this v_2 . Current flowing in coil one is i_1 , flowing in coil 2 is i_2 ; the induced voltage occurs coil one is e_1 while the induced voltage occurs coil 2 is v_2 . Then we have seen v_1 equal to $r_1 i_1$ plus $l_1 d i_1 / dt$ plus e_1 , and e_2 equal to $r_2 i_2$ plus $l_2 d i_2 / dt$ plus v_2 , and as in the case of an ideal transformer e_1 by e_2 equal to N_1 by N_2 .

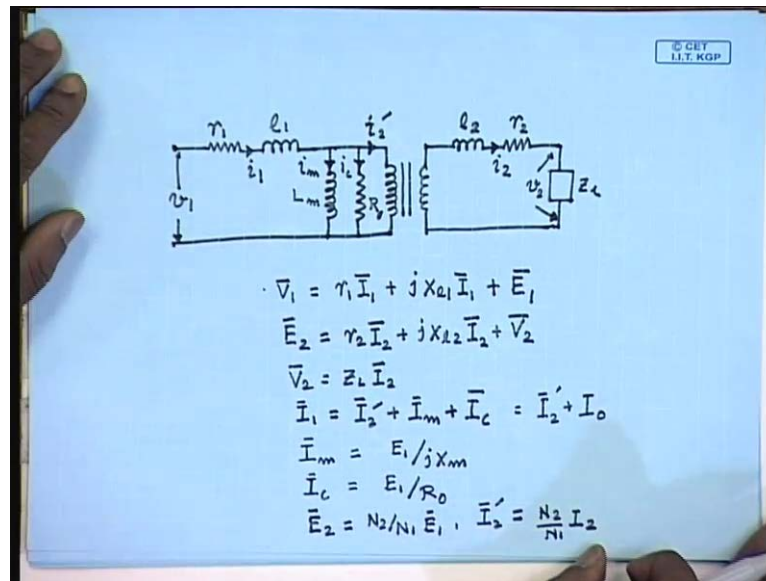
However this still does not take into account the magnetization current; that is the current to be drawn by a practical transformer even when i_2 is 0. This can be easily incorporated by assuming a magnetization inductance across the coil l_1 . This is so because the

induced voltage e_1 leads the flux by 90 degree. So, an inductance connected across e_1 the current flowing through the inductance connected across e_1 will lag e_1 by 90 degree and will be in phase with the core flux ϕ_m which is responsible for generating the core flux ϕ_m . So, the effect of finite permeability of a practical single phase transformer is taken care of by considering a magnetizing inductance across the winding and leakage inductance in series with the winding.

The last detail that needs to be incorporated in the model of a single phase transformer is the core losses. It is well known that when a ferromagnetic material is subjected to an alternating flux then two type of losses occur; one is the hysteresis loss, another is the eddy current loss. Both of them increase with increase in the flux density and the frequency of the alternative current waveform; although, these relations are not always linear for the hysteresis loss the loss is proportional power loss is proportional to the frequency, however not for the eddy current loss.

But in a normal application of a transformer normally the supply frequency and the voltage will be constant; therefore, for a given application there will not be variation with frequency since the frequency is constant. These losses can be approximated by losses occurring in some resistances fictitious resistances that are connected across the coil; therefore, in order to represent the losses that occur in a practical transformer some core loss resistance is connected across the coil that is parallel to the magnetization branch. Therefore the complete model of a practical transformer incorporating an ideal transformer looks as follows.

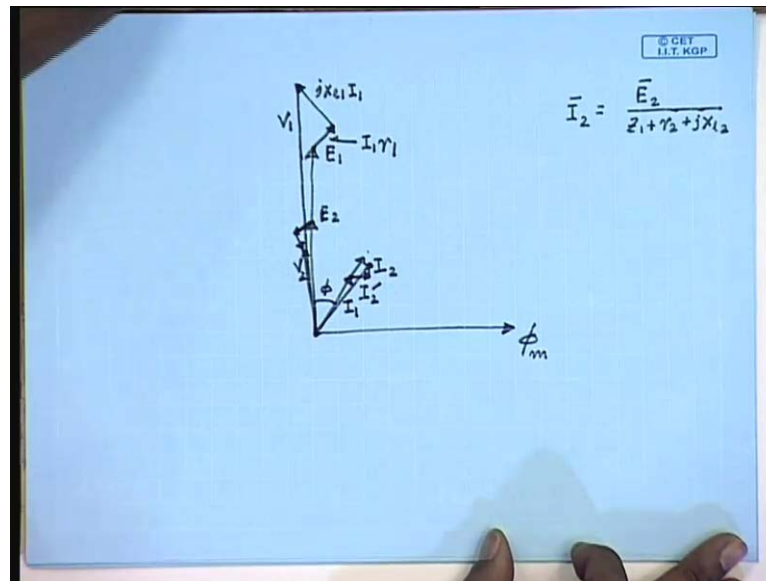
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First just put the winding resistance of coil one, then the leakage inductance of coil one and then the coil one of the ideal transformer. In order to take care of the finite magnetization current we connect a magnetization inductance and to care of the core loss we connect a core loss resistance. This is the ideal transformer. The secondary side similarly there will be a leakage inductance l_2 and the winding resistance r_2 . The applied voltage in the primary side is v_1 , current through the primary is i_1 , the magnetization current is i_m , the core loss component of the current i_c together they are called the no load current i_0 .

The current flowing through the ideal transformer is the reflection of the load current i_2' , and the current in the load circuit is i_2 , load voltage be v_2 . This is the complete model of a practical single phase transformer taking into account the non idealities which also incorporates an ideal transformer. In phasor form the equations can be written as v_1 equal to $r_1 I_1$ plus $j X_{l1} I_1$ plus E_1 , and E_2 equal to $r_2 I_2$ plus $j X_{l2} I_2$ plus v_2 . V_2 equal to $Z_L I_2$; I_1 equal to I_2' plus I_m plus I_c equal to I_2' plus I_0 . I_m equal to $E_1 / j X_m$, I_c equal to E_1 / R_0 ; E_2 equal to $N_2 / N_1 E_1$, and I_2' equal to $N_2 / N_1 I_2$. This set of equation defines the complete steady state model of a practical single phase transformer. Normally the steady state phasor relationships are described by a phasor diagram. Hence it will be interesting to draw the phasor diagram of a single phase transformer which is shown next.

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Again we draw the phasor diagram taking the core flux ϕ_m to be the reference phasor. Both E_1 and E_2 will lead I_m by 90 degree. Then the current I_2 is given by which will lag assume to lag E_2 by an angle ϕ . The phasor i_2 dash will be in phase with I_2 and will be in fact a scaled version of I_2 . The current I_1 can be obtained phasor sum of I_2 dashed with I_m which is in phase with ϕ_m and I_c which is in phase with E_1 which gives me the phasor I_1 . The voltage v_1 can be obtained on the first equation E_1 plus $I_1 r_1$ plus $j X_{L1} I_1$, this is $I_1 r_1$. So, this is the phasor v_1 ; similarly, phasor v_2 can be obtained by from the second equation it is E_2 minus $I_2 r_2$ minus $j X_{L2} I_2$ which can be used for solving any problem related to single phase transformer.

However, if we take a closer look at the circuit model of the single phase transformer that we have so derived, we will find that this is not very suitable for applying the known circuit analysis techniques since there is an ideal transformer between the two parts. Hence we cannot apply for example a kcl or kvl where the loop current is expected to flow across the ideal transformer. This path we cannot write a loop equation involving a path like this since we do not know the voltage drop across the ideal transformer; therefore, it will be more convenient if we can eliminate this ideal transformer.

This is done by referring the secondary of the ideal transformer to the primary or vice versa; the primary can also be referred to the secondary let us see the first option. By referring we mean we connect an equivalent circuit across the terminals of the ideal

transformers so that the current drawn by the equivalent circuit is the same as it is drawn by the ideal transformer. Let us see what should be that circuit.

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The whiteboard shows the following derivations:

$$\bar{I}_2 = \frac{\bar{E}_2}{Z_L + r_2 + jX_{L2}}$$

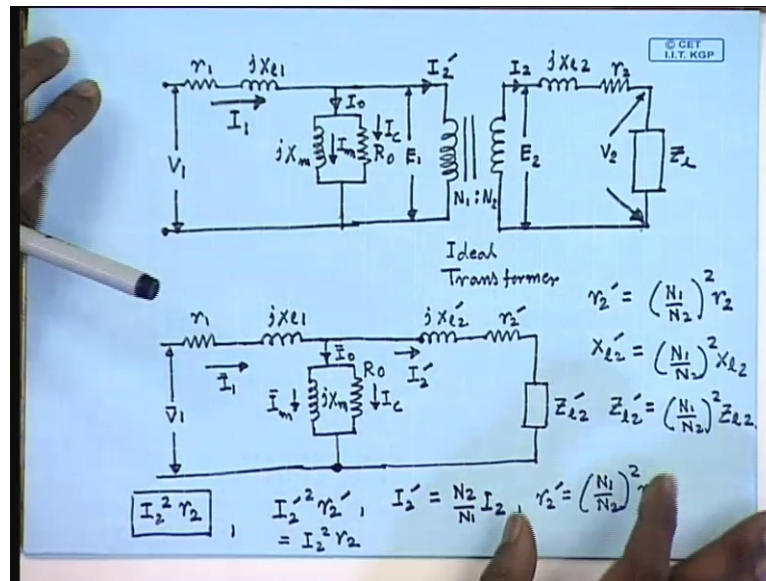
$$\bar{I}'_2 = \frac{N_2 \bar{I}_2}{N_1} = \frac{N_2 \bar{E}_2}{N_1 (Z_L + r_2 + jX_{L2})}$$

$$\bar{I}'_2 = \frac{\left(\frac{N_2}{N_1}\right)^2 \cdot \frac{N_1}{N_2} \bar{E}_2}{Z_L + r_2 + jX_{L2}}$$

$$\bar{I}'_2 = \frac{\bar{E}_1}{\left(\frac{N_1}{N_2}\right)^2 \{Z_L + r_2 + jX_{L2}\}}$$

The current I_2 is given by E_2 divided by Z_L plus r_2 plus jX_{L2} and I_2 dash equal to $N_2 I_2$ by N_1 ; therefore, I_2 dash equal to N_2 by $N_1 E_2$ divided by Z_L plus r_2 plus jX_{L2} or I can write I_2 dash to be N_2 by N_1 square into N_1 by $N_2 E_2$ divided by Z_L plus r_2 plus jX_{L2} , but N_1 by $N_2 E_2$ is equal to E_1 . Therefore I_2 dash equal to E_1 divided by N_1 by N_2 square into Z_L plus r_2 plus jX_{L2} . Now let us have a look at the model circuit model that we have obtained. The voltage across these two terminals is E_1 and the current drawn from these terminals is I_2 dash. Now the same current I_2 dash will flow provided an impedance of this form is connected across this terminals; therefore, we can replace the ideal transformer and the secondary circuit after that by an equivalent impedance given by this formula.

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Hence the original circuit model I_0 , I_1 , I_0 , I_m , I_c , I_2 and v_2 . This was the original circuit model of the practical single phase transformer which incorporated an ideal transformer with primary turns N_1 and secondary turns N_2 . This circuit model in argue can be replaced by an equivalent circuit model where up to this point there is no change, but the rest of the circuit is replaced by an equivalent circuit where the elements are given by, so that the current drawn from this branch is I_2' as in the case of the original model. The required equivalent impedances are related to the actual impedance's as follows; r_2' equal to N_1 by N_2 whole square r_2 , X_{l2}' equal to N_1 by N_2 square X_{l2} and Z_{L2}' equal to N_1 by N_2 square Z_{L2} .

That dashed quantities are called the referred impedance of the secondary side referred to the primary side, and the equivalent circuit thus obtained is called the exact equivalent circuit of a single phase transformer referred to the primary side; that is the side with suffix 1 and number of turns N_1 . Now let us see the actual secondary current to us I_2 . The power loss in the resistance was $I_2^2 r_2$. This was the actual power loss in the secondary circuit; in the equivalent circuit the power loss is $I_2'^2 r_2'$ but I_2' itself is equal to N_2 by $N_1 I_2$, and r_2' equal to N_1 by N_2 square r_2 . Therefore, $I_2'^2 r_2'$ is same as $I_2^2 r_2$; therefore, we see that the power consumed by the secondary resistance is not changed by referring this circuit. So, it preserves the power relations; what will be the referred secondary voltage v_2' ? We call this v_2' .

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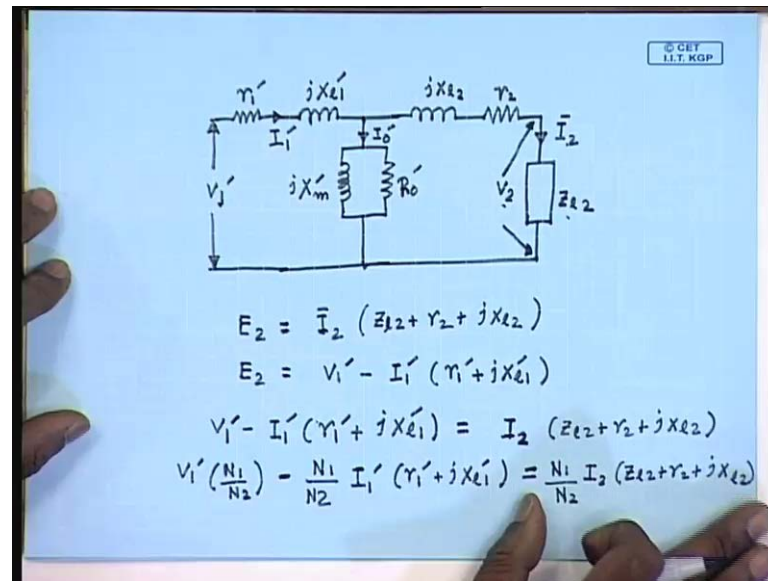
$$\begin{aligned} \bar{V}_2' &= \bar{I}_2' Z_{L2}' \\ \bar{I}_2' &= \frac{N_2}{N_1} \bar{I}_2, \quad Z_{L2}' = \left(\frac{N_1}{N_2}\right)^2 Z_{L2} \\ \bar{V}_2' &= \bar{I}_2' Z_{L2}' = \frac{N_2}{N_1} \bar{I}_2 \cdot \left(\frac{N_1}{N_2}\right)^2 Z_{L2} \\ &= \frac{N_1}{N_2} \bar{V}_2 \\ \bar{I}_2' &= \frac{N_2}{N_1} \bar{I}_2, \quad \bar{V}_2' = \frac{N_1}{N_2} \bar{V}_2 \\ \bar{V}_2' \bar{I}_2' &= \bar{V}_2 \bar{I}_2 \end{aligned}$$

When \bar{v}_2 dash equal to \bar{I}_2 dash Z_{L2} dash, but \bar{I}_2 dash equal to N_2 by N_1 \bar{I}_2 , and Z_{L2} dash equal to N_1 by N_2 whole square Z_{L2} . Therefore, \bar{v}_2 dash equal to \bar{I}_2 dash Z_{L2} dash is equal to N_2 by N_1 \bar{I}_2 into N_1 by N_2 whole square Z_{L2} equal to N_1 by N_2 \bar{v}_2 . So, we have \bar{I}_2 dash equal to N_2 by N_1 into \bar{I}_2 and \bar{v}_2 dash equal to N_1 by N_2 into \bar{v}_2 ; therefore, \bar{v}_2 dash \bar{I}_2 dash equal to $\bar{v}_2 \bar{I}_2$. So, the kvl consumed by the referred circuit referred load circuit is same as the kvl consumed by the original load circuit. So, you have seen that the load circuit can be referred to the source side by multiplying the load side parameters values that is the impedance's by the trans ratio square. Similar thing can be done for the source side; that is instead of referring the rest of the circuit across this terminal it is possible to refer the left of the circuit across this terminal; in which case the secondary side impedance's voltages and current will remain same while the impedance's of the primary side will change.

Let us see what will be the relations. The actual circuit model which is given by this diagram, we now want to replace with by an equivalent circuit with an equivalent network connected across this two terminal. This circuit which is also an equivalent circuit referred to the primary side was obtained by replacing the right hand side of the circuit after this point by an equivalent impedance, and this circuit was called the exact equivalent circuit of the transformer referred to the primary side since the impedance on the secondary side where referred by the turns ratio. Now we want to derive an equivalent circuit of the transformer referred to the secondary side where the secondary

impedance's will remain as it is; however, the primary source and impedance will be referred to the secondary side. Although it is possible to connect different types of impedance's we would like to detail the structure or the topology of the circuit on the primary side as far as possible; therefore, we assume the equivalent circuit to be as follows.

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The secondary side does not change. We would like to retain the structure of the primary circuit; therefore, you will have an equivalent impedance in series, equivalent inductance in series and call it jX_{l1} . The shunt branch consisting of a shunt reactor jX_m and R_0 and a series resistance r_1 connected to a source v_1 . Our objective is to find out an expression of the dashed quantity so that when the dashed quantities are applied to the circuit the secondary current remain I_2 , and the secondary voltage becomes v_2 when the load connected is Z_{l2} . Let this current be I_1 , the voltage E_2 equal to I_2 into also E_2 equal to; therefore, multiplying both sides by N_1 by N_2 we can write this.

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$$\begin{aligned}
 & \left(\frac{N_1}{N_2}\right)V_1' - \frac{N_1}{N_2} I_1' (r_1' + jX_{L1}') \\
 &= \frac{N_2}{N_1} I_2 \cdot \left(\frac{N_1}{N_2}\right)^2 (Z_{L2} + r_2 + jX_{L2}) \\
 &= I_2' [Z_{L2}' + r_2' + jX_{L2}'] = E_1 = V_1 - I_1 (r_1 + jX_{L1}) \\
 &V_1 - I_1 (r_1 + jX_{L1}) = [V_1' - I_1' (r_1' + jX_{L1}')] \frac{N_1}{N_2} \\
 &V_1 = \frac{N_1}{N_2} V_1' \quad \text{OR} \quad V_1' = \frac{N_2}{N_1} V_1 \\
 &I_1 (r_1 + jX_{L1}) = \frac{N_1}{N_2} I_1' (r_1' + jX_{L1}') \\
 &I_1^2 r_1 = I_1'^2 r_1' \quad I_1' = \frac{N_1}{N_2} I_1 \quad \& \quad r_1' = \left(\frac{N_2}{N_1}\right)^2 r_1
 \end{aligned}$$

Or it can be written as but $N_2 I_2$ divided by N_1 equal to I_2' and N_1 by N_2 square into this impedance's are. However from our previous discussion we have seen that this quantity I_2' into $Z_{L2}' + r_2' + jX_{L2}'$ this is equal to E_1 and is given by v_1 minus $i_1 r_1$ plus jX_{L1} . Therefore, v_1 minus $i_1 r_1$ plus jX_{L1} equal to v_1 dash minus i_1 dash r_1 plus jX_{L1} into N_1 by N_2 . Equating terms we can say v_1 equal to N_1 by N_2 into v_1 dash or v_1 dash equal to N_2 by N_1 into v_1 . Similarly $I_1 r_1 + jX_{L1}$ equal to N_1 by N_2 I_1 dash into r_1 plus dash jX_{L1} dash.

If we want the power relations power consumed by the resistance r_1 should be same as the power consumed by the resistance r_1 dash in the equivalent circuit, then we must have $I_1^2 r_1$ equal to $I_1'^2 r_1'$. This will be ensured if we define I_1 dash equal to N_2 by N_1 into I_1 and r_1 dash equal to N_2 by N_1 square into r_1 ; I_1 dash equal to N_1 by N_2 square into I_1 and r_1 dash square equal to N_2 by N_1 square into r_1 .

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$$\left. \begin{aligned} r_1' &= \left(\frac{N_2}{N_1}\right)^2 r_1 \\ X_{e1}' &= \left(\frac{N_2}{N_1}\right)^2 X_{e1} \end{aligned} \right\} V_1' = \frac{N_2}{N_1} V_1$$

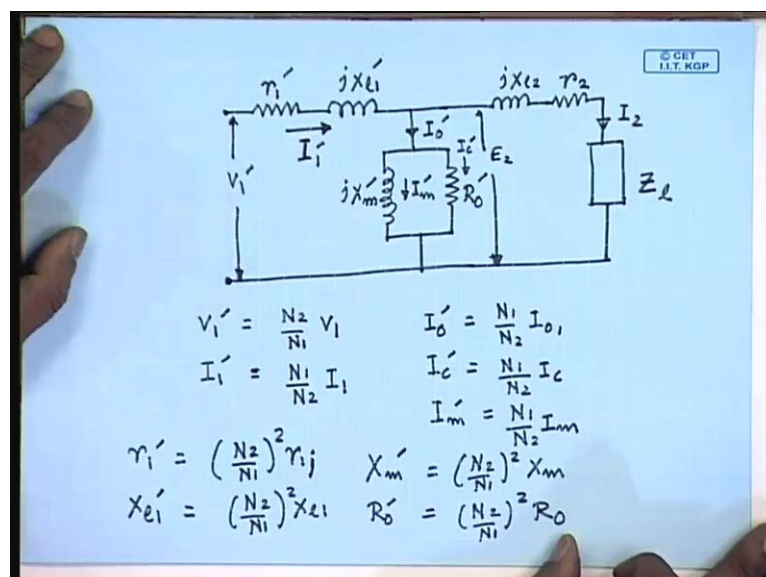
$$\frac{E_2^2}{R_0} = \frac{E_1^2}{R_0} \quad \text{or } R_0' = \left(\frac{E_2}{E_1}\right)^2 R_0$$

$$E_2/E_1 = \frac{N_2}{N_1} \quad \therefore R_0' = \left(\frac{N_2}{N_1}\right)^2 R_0$$

$$X_m' = \left(\frac{N_2}{N_1}\right)^2 X_m$$

Therefore we will say r_1' will be equal to N_2 by N_1 square into r_1 . Similarly X_{e1}' equal to N_2 by N_1 square into X_{e1} and we have already obtained V_1' equal to N_2 by N_1 into V_1 . Regarding the shunt in branches again if we want the impedance's I mean the power balance to be maintained then I should have E_2 square by R_0' should be equal to E_1 square by R_0 , or r_0' should be equal to E_2 by E_1 whole square into R_0 , but E_2 by E_1 equal to N_2 by N_1 ; therefore, R_0' is equal to N_2 by N_1 square R_0 . Similarly, X_m' equal to N_2 by N_1 whole square X_m .

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Therefore, the equivalent circuit of the single phase transformer referred to the secondary or the load side will be similar to the previous circuit except the parameter values will now be different; it is given by a similar circuit. Relationships are $v_1' = \frac{N_2}{N_1} v_1$, $I_1' = \frac{N_1}{N_2} I_1$, $I_0' = \frac{N_1}{N_2} I_0$, $I_c' = \frac{N_1}{N_2} I_c$, $I_m' = \frac{N_1}{N_2} I_m$. The parameters are $r_1' = \frac{N_2}{N_1} r_1$, $X_{11}' = \frac{N_2}{N_1} X_{11}$.

$X_m' = \frac{N_2}{N_1} X_m$, and $r_0' = \frac{N_2}{N_1} r_0$. So, this is the equivalent circuit of a practical single phase transformer referred to the secondary or the load side while this was the equivalent circuit of the same single phase transformer referred to the primary or the source side. They are identical as far as the circuit structure are concerned they are identical except that the parameter values of the two circuits are different.